



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named
Inventor : Jonathan William Byrn et al

Appln. No. : 10/664,137

Filed : September 17, 2003

For : CUSTOM CLOCK INTERCONNECTS
ON A STANDARDIZED SILICON
PLATFORM

Docket No.: 02-4712/L13.12-0228

Group Art Unit: 2827

Examiner:

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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PATENT ATTORNEY

Sir:

The patents or publications listed on the enclosed PTO Form-1449 are submitted pursuant to 37 C.F.R. § 1.97. Copies of the patents or publications cited are enclosed, except as waived by the Official Gazette notice of August 5, 2003 regarding copies of US Patents and Publications.

The Director is authorized to charge any fee deficiency required by this paper or credit any overpayment to Deposit Account No. 23-1123. A duplicate copy of this communication is enclosed.

Respectfully submitted,

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FORM PTO-1449	Atty. Docket No.: 02-4712/L13.12-0228	Appl. No.: 10/664,137
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	First Named Inventor:	
	Jonathan William Byrn et al.	
	Filing Date	Group Art:
	September 17, 2003	2827

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Date	Name	Class	Sub Class	Filing Date If Appropriate
AA						
AB						
AC						
AD						
AE						
AF						
AG						
AH						

FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Class	Sub Class	Translation Yes No
AI						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AJ	Rapid Chip™ Technology, "Fast Custom Silicon through Platform-Based Design" of LSI LOGIC, Copyright 2002, dated April 2003, pages 1-14
AK	Rapid Chip, "platforms," "New family of RapidChip Foundation Slices" of LSI LOGIC, May 1, 2003, website pages 1 and 2
AL	Rapid Chip, "Semiconductor Platform," copyright 2002 by LSI Logic Corporation, 4 pages
AM	Rapid Chip, "platforms," "RapidChip frequently asked questions" of LSI LOGIC, May 1, 2003, website pages 1 and 2
AN	Rapid Chip, "platforms," "Redefining the way chips are made" of LSI LOGIC, May 1, 2003, website pages 1 and 2
AO	Rapid Chip, "Fast facts" of LSI LOGIC, May 1, 2003, website page 1

EXAMINER:	DATE CONSIDERED:
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.